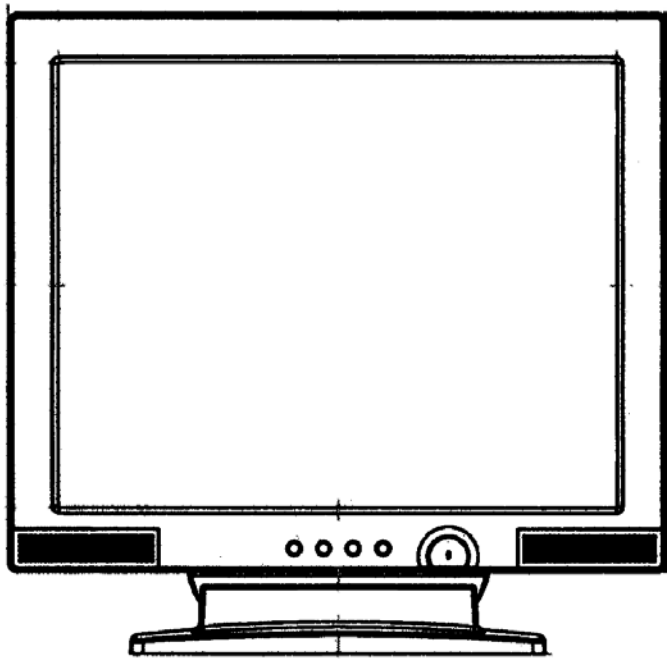
Service Manual



Model : Belinea 101710

MAXDATA Systeme GmbH

Elbestr. 16

45768 Marl / Germany

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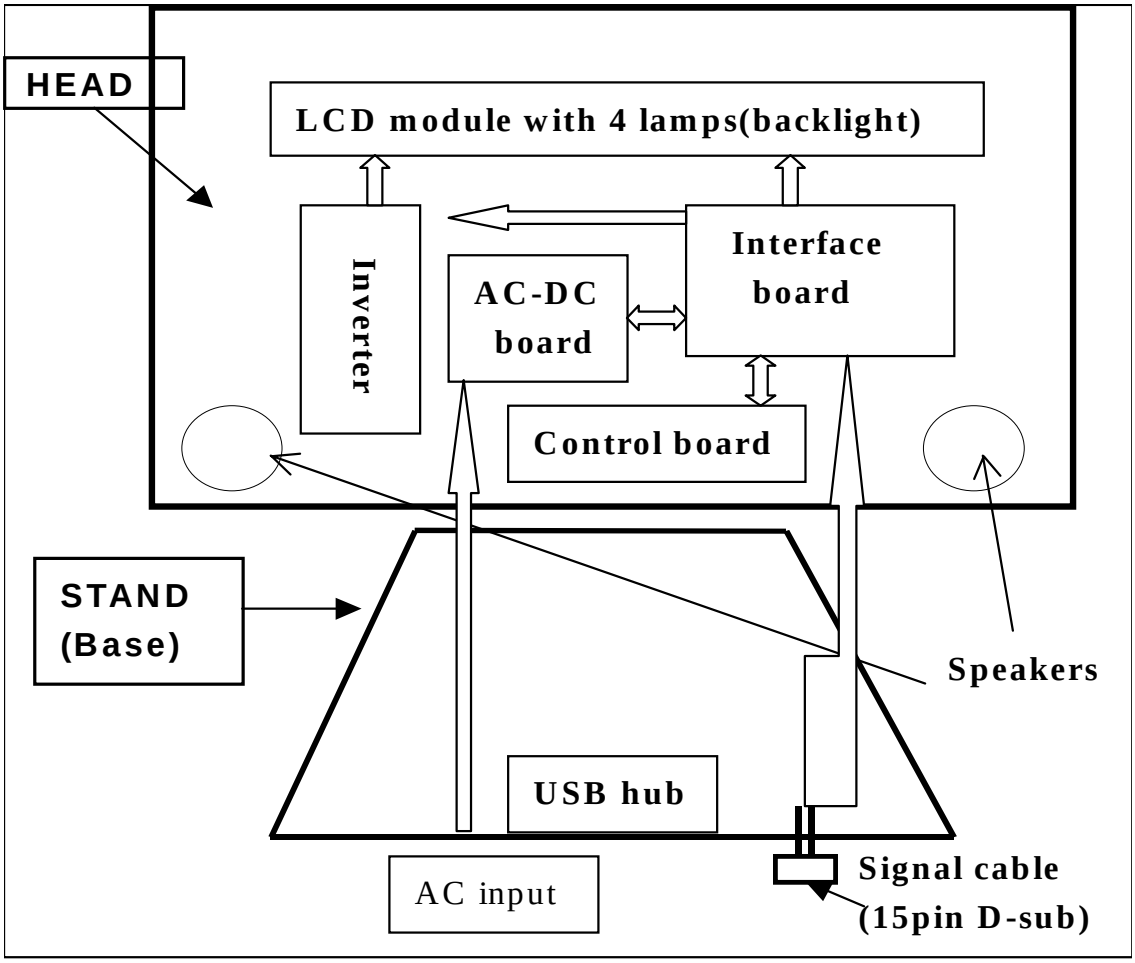
1.1 Introduction

The Belinea 101710 is an 17" SXGA(1280x1024) 24 bits color TFT LCD monitor with multi-media function and an optional USB hub. It's an analog interface LCD monitor with an undetachable 15 pins D-sub signal cable and it's compliant with VESA specification to offer a smart power management and power saving function. It also offers OSD menu for users to control the adjustable items and get some information about this monitor, and the best function is to offer users an easy method to set all adjustable items well just by pressing one key, we called it "i-key"(intelligent key) which can auto adjusting all controlled items.

1.2 Block diagram

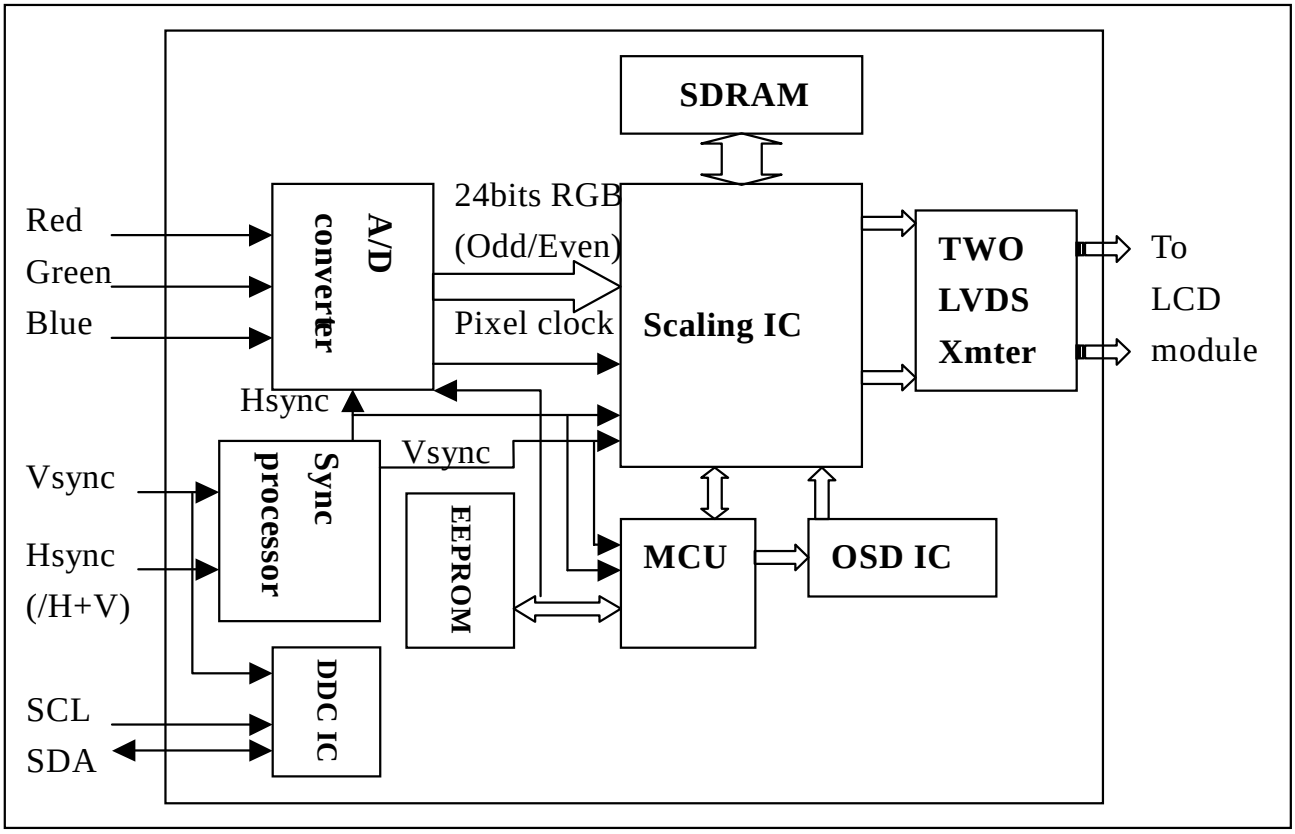
The Belinea 101710 consists of a head and a stand (base). The head consists of a LCD module with 4 lamps, an Inverter, an Interface bard, a AC-DC board, a Control board and a microphone. The stand consists of an AC-DC board, an Audio board, two speakers and an optional USB hub. The block diagram is shown as below,



1.3 Circuit operation theory

1.3.1 Interface board diagram

Interface Board



1.3.1.1 Circuit operation theory

A basic operation theory for this interface board is to convert analog signals of Red, Green and Blue to digital signals of Red, Green and Blue, and by the internal PLL circuit of A/D converter to generate the pixel clock to output to the scaling IC, then the scaling IC use the SDRAM to be the frame buffer to process the different input signals which are operating in the specification of the A/D converter and the scaling IC, finally the scaling IC output the digital RGB data, the fixed frequency of Hsync, Vsync and pixel clock to LCD panel driver IC by LVDS transmitter.

1.3.1.2 IC introduction

1.) DDC(Display Data Channel) IC: We use ATMEL 24C21 to support DDC1/2B function, it will store the information related to LCD monitor and PC can read them by "Vsync and SDA" serial communication for DDC1 or I^2C communication for DDC2B.

2.) Sync Processor: Because the A/D converter and the scaling IC of this board can't separate composite Sync into Hsync and Vsync by themselves, we use 14053B to output the pure Hsync and Vsync respectively to them to solve this application problem.

3.) A/D converter: We use AD9884 of ADI which can support maximum 140MHz pixel rate to convert analog signals of RGB to 24bits digital RGB signals and generate a pixel clock signal by Hsync and its internal PLL circuit, then transmit these digital signals to Scaling IC.

4.) Scaling IC: We use MX88L282 of Macronix and by its frame buffer function to process the digital RGB signals to maximum 75Hz refresh rate at 1280 by 1024 resolution(about 135MHz pixel rate), it also support a ratio expansion function to display a full screen when the resolution of input signals are less than 1280 by 1024.
5.) SDRAM: We use 3 pieces of 2M bytes SDRAM to be the frame buffer of the scaling IC to process maximum 1280 by 1024 resolution of 24 bits RGB data.
6.) MCU: We use Myson MTV112V to controls all the functions of this interface board, just like the mode detecting, OSD display setting, the adjustable items, adjusted data storage, DPMS control, the external RS232 communication and the scaling IC
7.) EEPROM: We use 24C04 to store all the adjustable data , and we divide it into two parts for the storage of factory settings and user settings.
8.) OSD IC: We use LSC3852 of Motorolla to do "On Screen Display" function.
9.) LVDS transmitter IC: We use two pieces of THC63LVDM83A of THine to transmit all the digital data to the LCD panel driver IC because this driver IC has two pieces of built-in THC63LVDM83A (LVDS receiver IC).

1.3.2 AC-DC board diagram

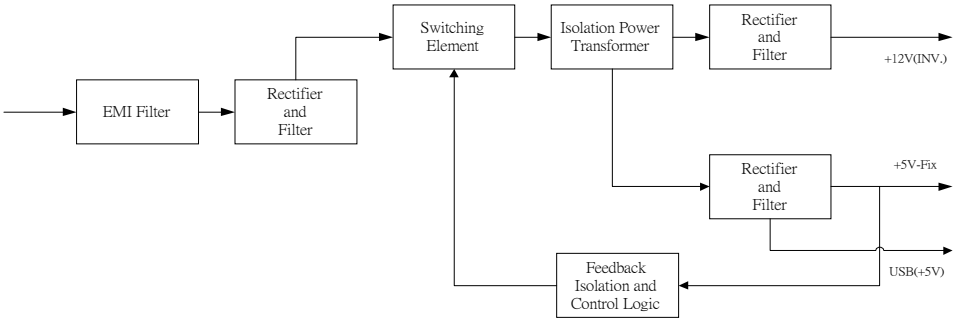


Fig. 1

1.3.2.1 EMI Filter

This circuit (fig. 2) is designed to inhibit electric and magnetic interference for Meet FCC, VDE, VCCI standard requirements

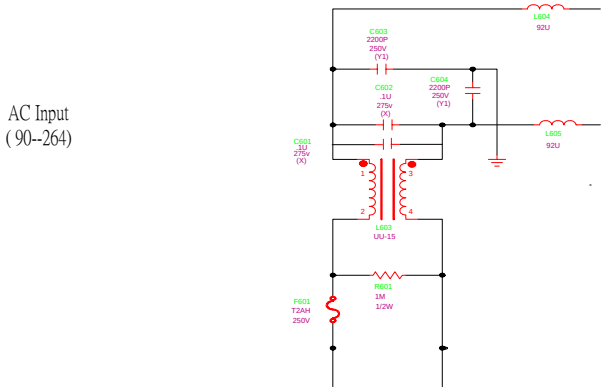


Fig. 2

1.3.2.2 Rectifier and filter

AC Voltage (90-264V) is rectified and filtered by BD601, C605 (See Fig 3) and the DC Output voltage is 1.4*(AC input). (See Fig.3)

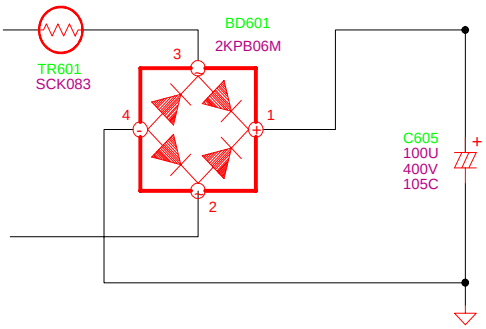


Fig. 3

1.3.2.3 Switching element and Isolation power transformer

When the Q601 turns on, energy is stored in the transformer. During Q601 OFF period, the stored energy is delivered to the secondary of transformer. R604, C606 and D601 is a snubber circuit. R605 is current sense resistor to control output power. (See Fig.4)

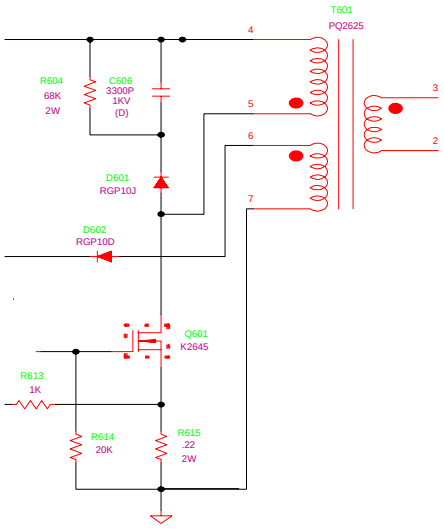


Fig. 4

1.3.2.4 Rectifier and filter

D701 and C701 are to produce DC output. L701 and C702 are to suppress high Frequency switching spikes. R702 is a dummy load. (See Fig.5)

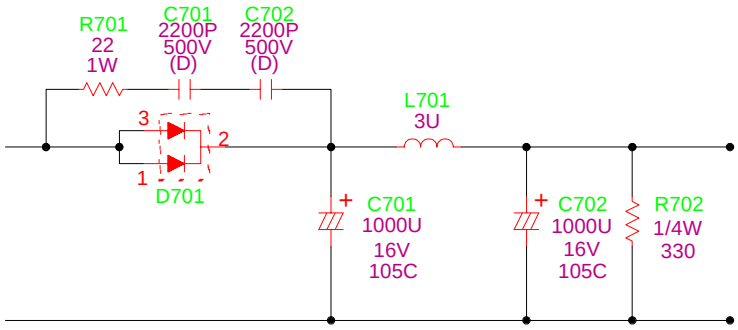


Fig. 5

1.3.2.5 Control circuit

The current mode control IC UC3843A is used to control PWM. When the VCC terminal of it gets 8.4V, IC601 turns on. +5V will be set up at Pin8 through soft start circuit includes R617, C611, D606 and D605. R616 and C610 generate a fixed frequency sawtooth wave at Pin4.

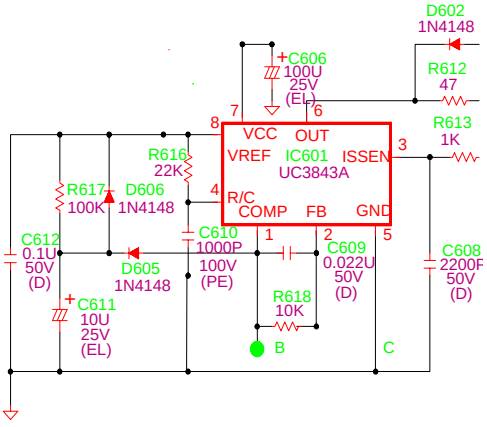


Fig. 6

1.3.2.6 Feedback circuit

TLP721F is a optocoupler and TL431 is a shunt regulation. They are used to detect the output voltage change and primany and secondary isolation. When output voltage changes, the feedback voltage will be compared and duty cycle will be decided to control the correct output voltage. (See Fig.7)

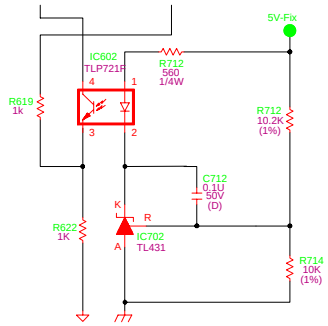
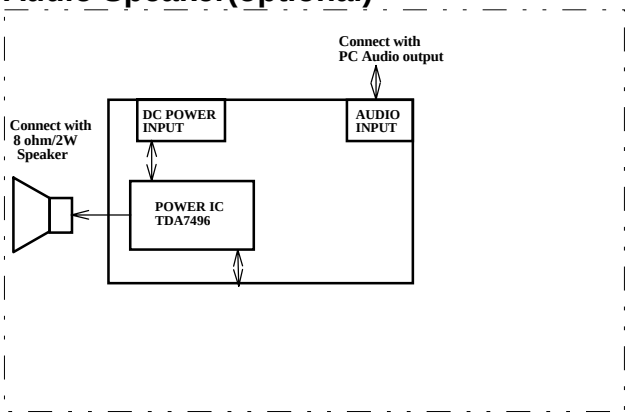


Fig. 7

1.3.3 Audio Speaker(optional)



The Audio Speaker is consinst of an Audio board. The Audio Speaker have DC Volumn control, use 28mmX40mm Speaker (2W/per chennal), power supply from AC-DC board and Audio input from PC Audio output (Line Out) .

1.3.3.1 Power IC

Use ST POWER IC TDA7496.The IC are stereo AB Class output amplifiers with DC Volume control. The devices are designed for use in TV and monitor, but are also suitable for battery-Fed portable recorders and radios. Use +12V from AC-DC Board and connect speaker to offer 1W per chennal.

1.3.3.2 DC Power Input

To supply +12V to be VCC source Voltage for TDA7496 and built-in AC-DC board.

1.3.3.3 Audio Input

connect with PC Audio output in 3.5mm to 3.5mm signal line.

1.3.3.4 Speaker

Use 8 ohm and 28mmX40mm speaker (2W/per chennal)

1.3.3.5 DC Volumn Control

The voltage range is 1 – 5 V (From MC)

Alignment procedure (for function adjustment)

1. Enter factory area (press “enter” & “exit” & “power on”).
2. selected timing which has star sign(*) have to adjust. (see figure 1).
3. clear user area.
4. adjust the contrast and the brightness bar to “50”.
5. Press I-key to adjust the image.
6. check the image if phase is not perfect adjust it to the best step.
7. Save factory parameter then turn off power.
8. Clear user area.
9. Turn on power enter user area.

Figure 1.

Incoming display mode(Input timing)					Multi-scan operation
Resolution	Horizontal Frequency (KHz)	Vertical Frequency (Hz)	Dot Clock Frequency (MHz)	Remark	Actual display resolution
*640x350	31.47(P)	70.08(N)	25.17	DOS	full screen 1280x1024
*720x400	31.47(N)	70.08(P)	28.32	DOS	
*640x480	31.47(N)	60.00(N)	25.18	DOS	
*640x480	35.00(N)	67.00(N)	30.24	Macintosh	
*640x480	37.86(N)	72.80(N)	31.5	VESA	
*640x480	37.50(N)	75.00(N)	31.5	VESA	
*800x600	37.88(P)	60.32(P)	40.00	VESA	
*800x600	48.08(P)	72.19(P)	50.00	VESA	
*800x600	46.86(P)	75.00(P)	49.50	VESA	
832X624	49.72(N)	74.55(N)	57.29	Macintosh	
*1024x768	48.36(N)	60.00(N)	65.00	VESA	
1024x768	56.48(N)	70.10(N)	75.00	VESA	
*1024x768	60.02(P)	75.00(P)	78.75	VESA	
1024X768	60.24(N)	74.93(N)	80.00	Macintosh	
1152x864	67.50(P)	75.00(P)	108.00	VESA	
1152x870	68.68(N)	75.06(N)	100.00	Macintoch	
1152x900	61.80(P)	66.00(P)	92.94	SUN 66	
1152x900	71.81(N)	76.14(N)	108.00	SUN	
*1280x1024	64.00(P)	60.00(P)	108.00	VESA	
*1280x1024	75.83(N)	71.53(N)	128.00	IBM1	
*1280x1024	80.00(P)	75.00(P)	135.00	VESA	
1280x1024	81.18(N)	76.16(N)	135.09	SPARC2	

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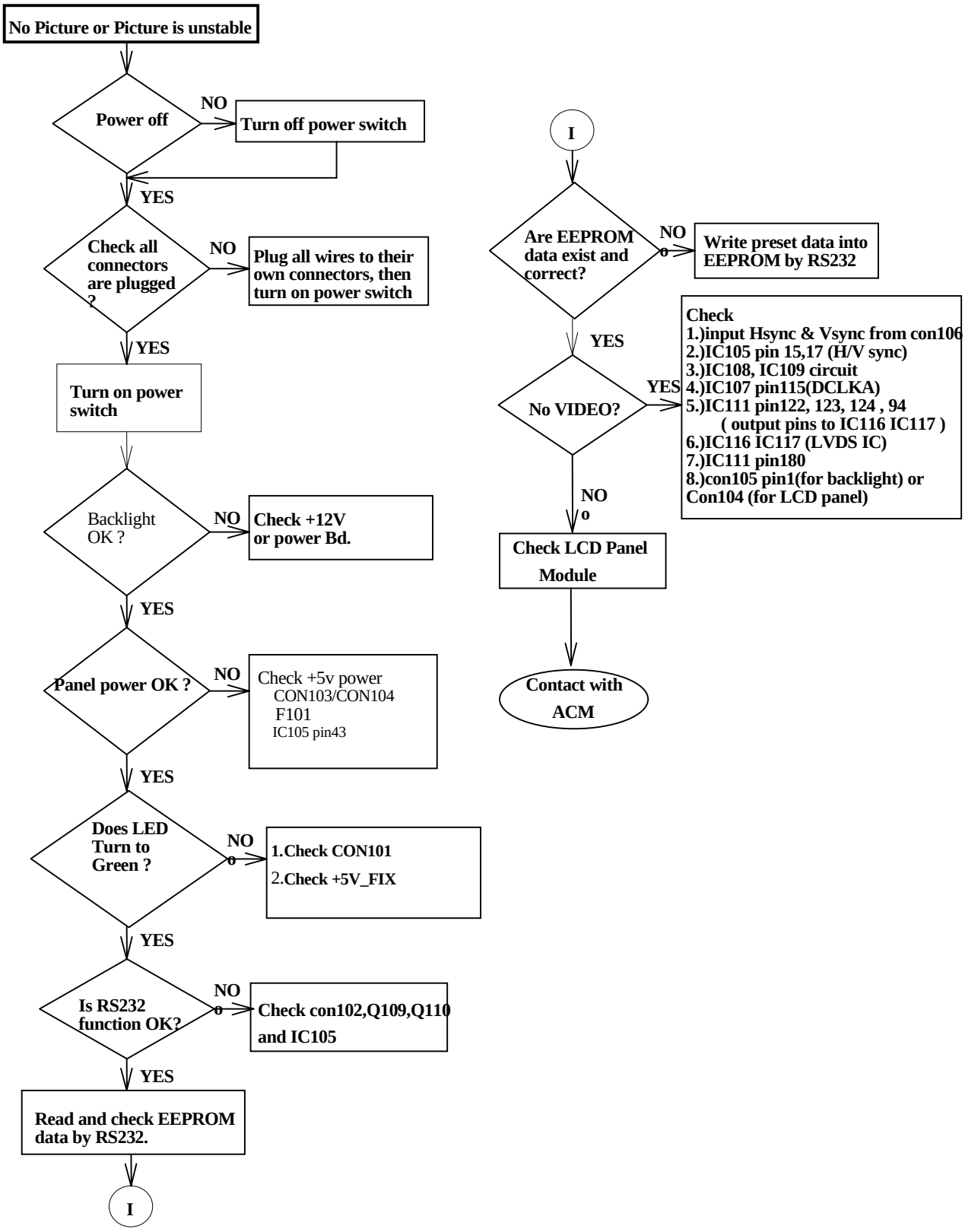
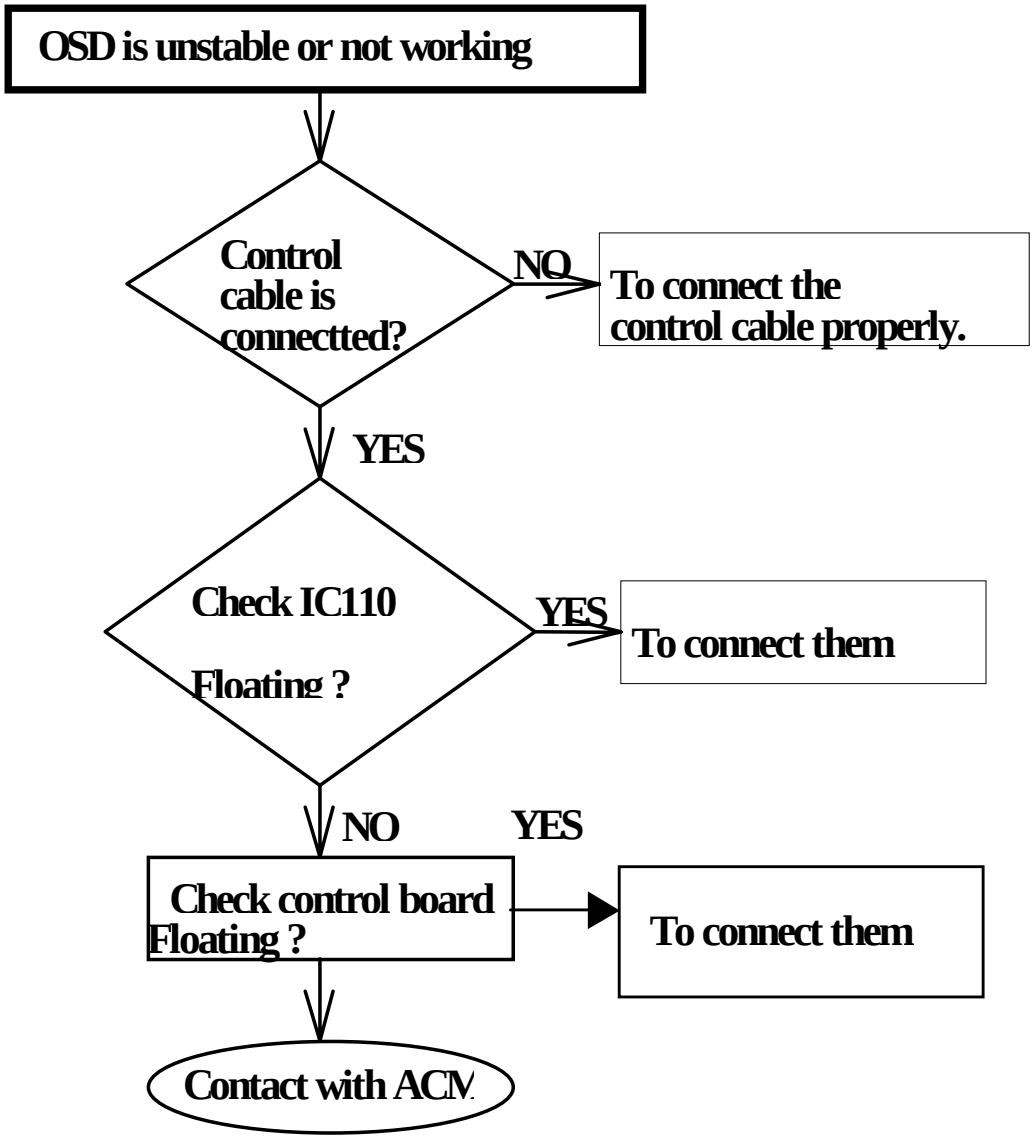


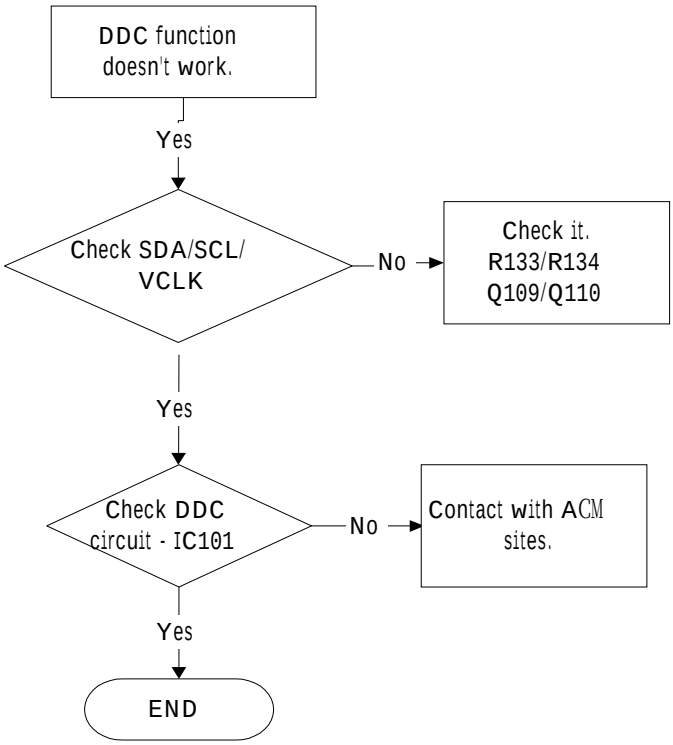
4.2 BUTTON function

4.2.1 Control Board

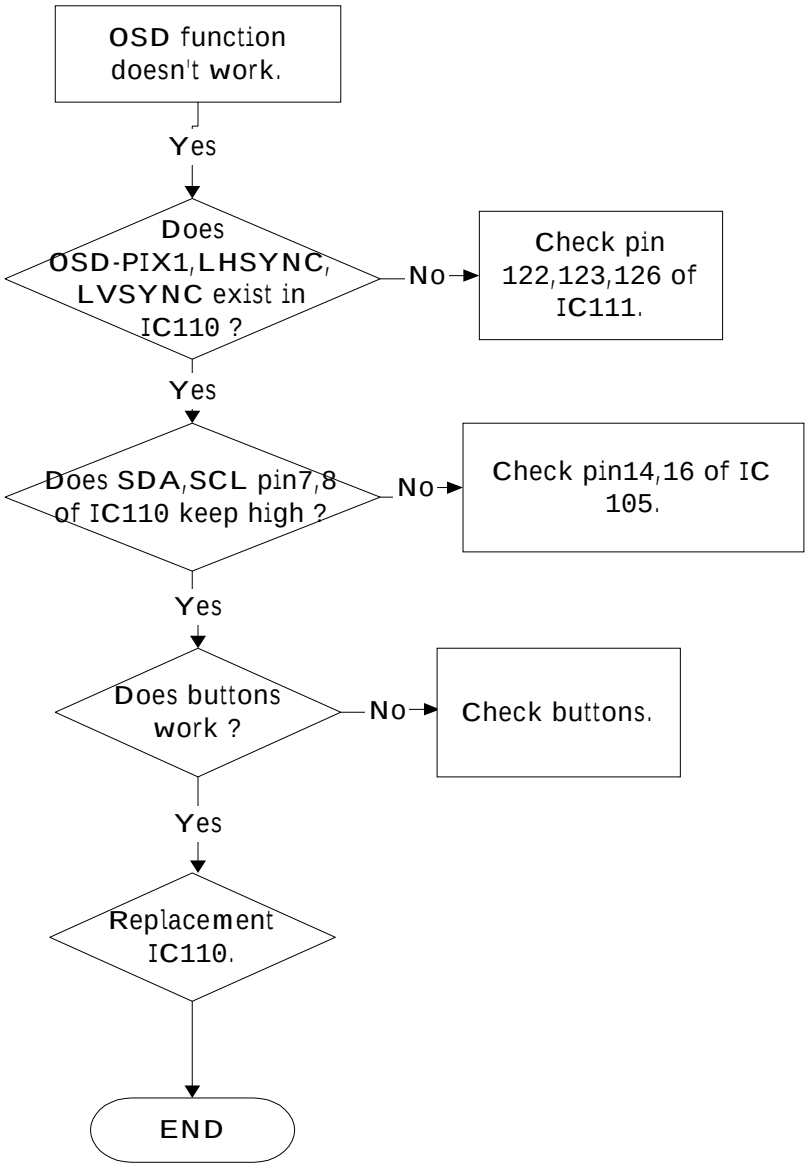


4.3 DDC function

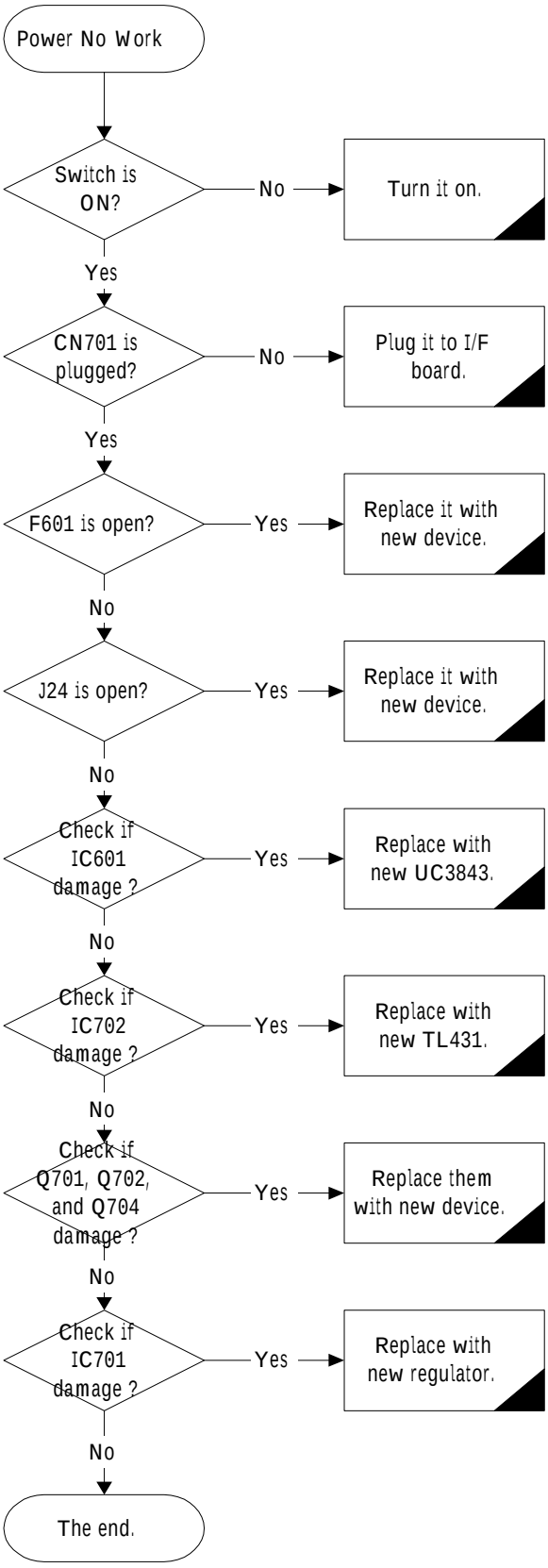
4.3.1 DDC

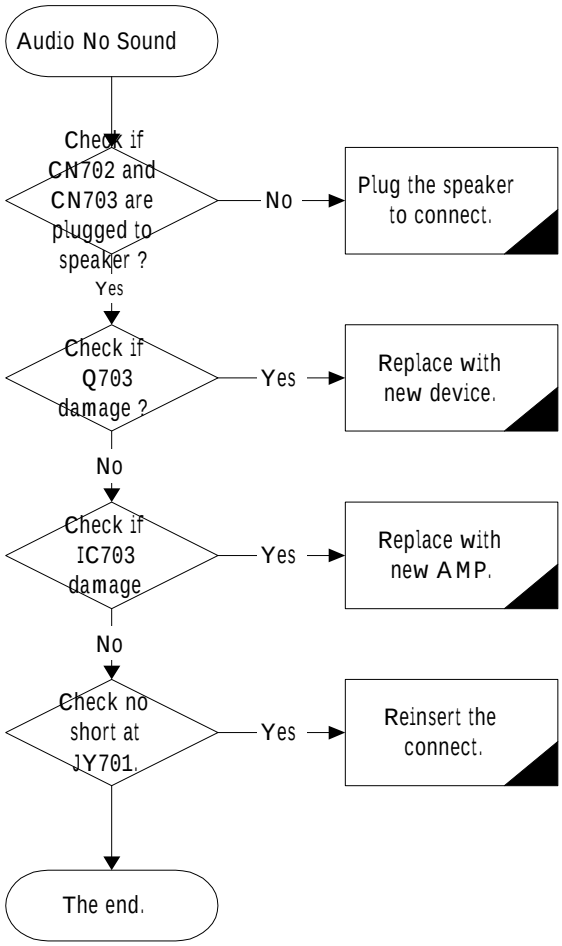


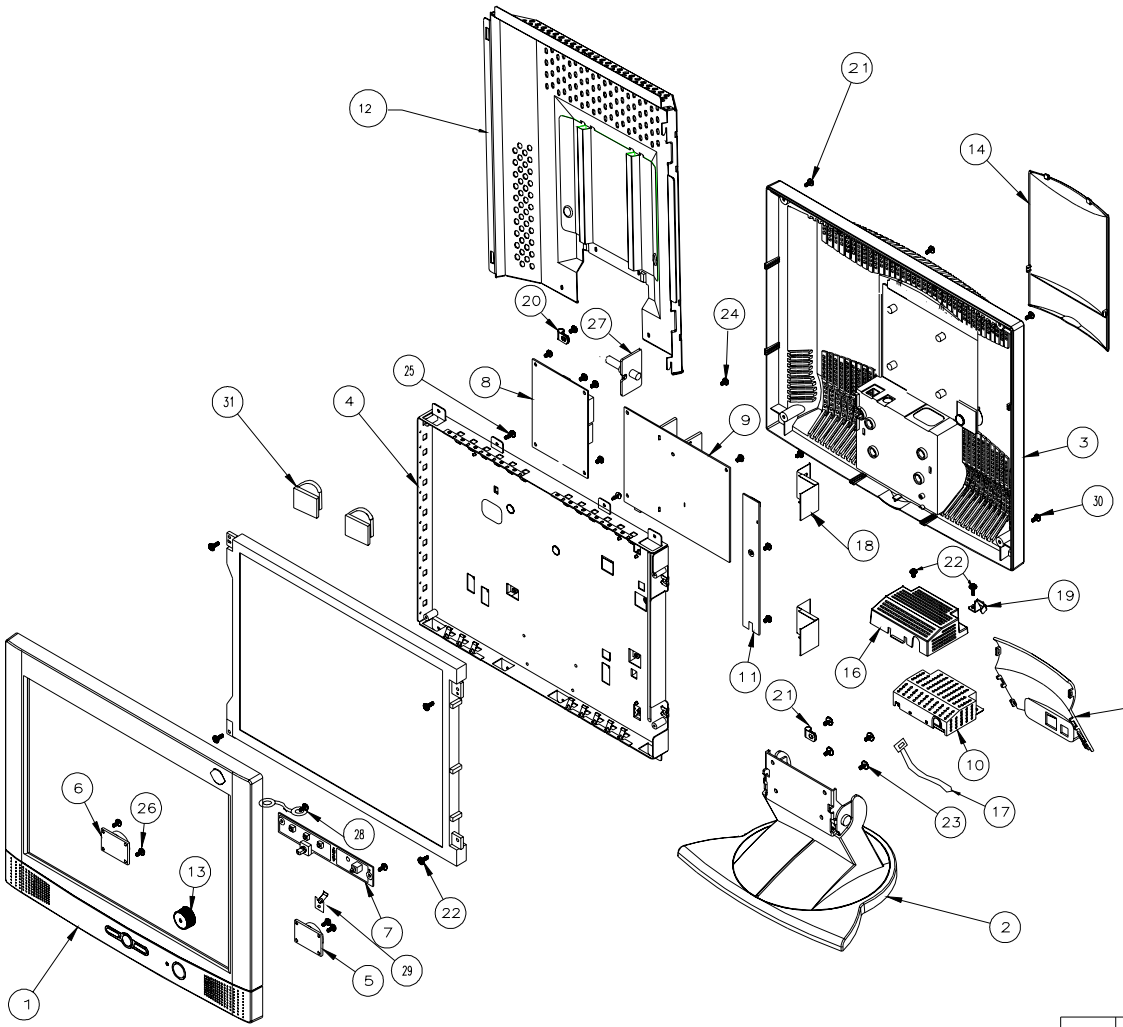
4.4 OSD function



4.5 Power Board with audio







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2	60.L0704.001	ASSY BASE	1
3	39.L0701.001	ASSY COVER REAR	1
4	60.L1601.001	ASSY BKT-R	1
5	65.LXXXX.001	ASSY SP+WIRE 370MM	1
6	65.LXXXX.001	ASSY SP+WIRE 200MM	1
7	55.L1402.001	CONTROL BD	1
8	55.L0703.001	INTERFACE BD	1
9	55.L1404.001	SPS BD	1
10	55.L1405.001	USB BD	1
11	54.05023.221	INVERT BD	1
12	60.L0706.001	ASSY SHD CVR	1
13	42.L1401.001	KNOB VR	1
14	42.L1403.001	CVR ACC	1
15	42.L1405.001	CVR BASE	1
16	42.L1407.001	CVR USB	1
17	38.05001.001	CABLE TIE YJ-98	1
18	31.L0702.001	SHD PANEL	2
19	31.L1301.001	CLIP BASE	1
20	34.74703.011	CLAMP CABLE	1
21	34.74803.001	CLIP CABLE	1
22	86.00110.140	SCREW M3X10	6
23	86.00010.161	SCREW TAPTILE M4X8	5
24	86.7A524.6R0	SCREW M3X6	19
25	86.WA314.8R0	SCREW M3X1.34P 8L	6
26	86.VG524.6R0	SCRW M3 TAP 6L	4
27	50.L1603.001	SIGNAL CABLE	1
28	50.JOXXX.001	CABLE	1
29	31.L0705.001	SPRING SPK	1
30	86.XA514.120	SCRW M3 FH TAP	4
31	42.L0703.001	CLIP FW-2	2
32			

UNIT OR ASSEMBLY	
DRN	CHARLES HO
DSN	CHARLES HO
CKD	AARON HO
APPD	JIMMY HSIEH

NAME: ASSY DRAWING

MATERIAL

FINISH

REV	DESCRIPTION	SIGN	DATE

SCALE | DIM IN | M.M

PARTS TOLERANCE(±)

Class	A	B	C	D	E	F	G
<6mm	0.05	0.1	0.1	0.2	0.2	0.4	0.4
6~25mm	0.08	0.15	0.15	0.3	0.3	0.6	0.6
25~80mm	0.12	0.2	0.25	0.4	0.5	0.8	1.0
80~250mm	0.20	0.3	0.4	0.6	0.8	1.2	1.5
250~800mm	0.5	0.6	0.8	1.0	1.5	2.0	3.0
>800mm	1.0	1.0	1.5	1.7	3.0	4.0	6.0

Length | Tolerance(mm)

MOLD NO.

SHEET 1 OF 1

DRAWING NO. 99.L1672.001

5. Spare Parts List

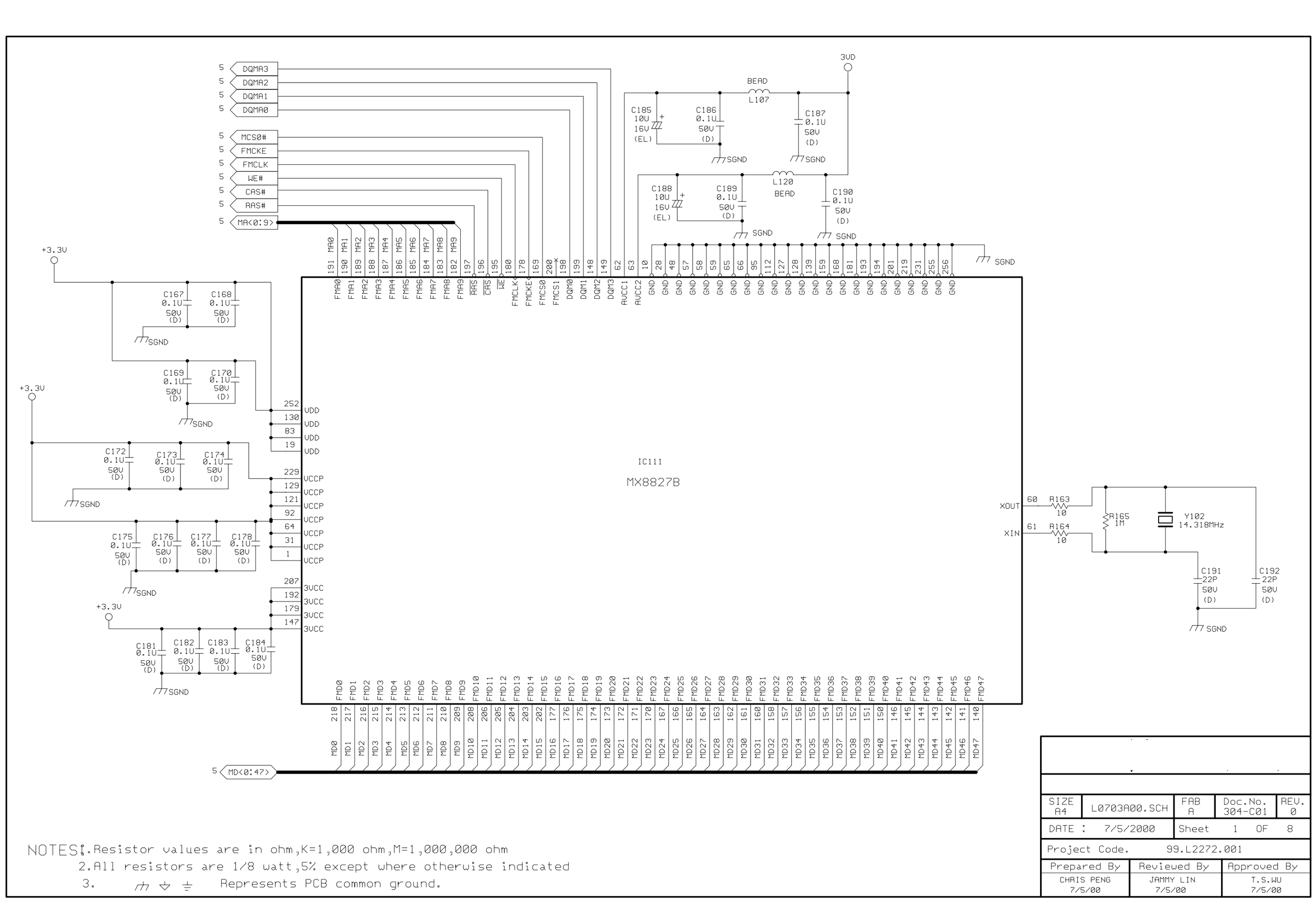
P/N: 99.L2272.XWG

ITEM	PART NO.	DESCRIPTION	LOCATION
1	55.L0704.001	SPS BD FP750(MI)	
2	04.07496.010	IC AMP TDA7496 SIP 15P	IC703
3	05.00123.010	IC OPTO PC123F1 DIP 4P	IC602
4	06.00100.052	SCR MCR100-6 400V 0.8A TO-92T	IC603
5	74.00431.A3B	IC V.R ZR431 SOT-23	IC702
6	74.03843.07K	IC PWM CTRL UC3843ADM SOIC 8P	IC601
7	04.3RF23.030	IC V.R PQ3RF23 3.3V TO-220	IC701
8	55.L2203.011	INTERFACE BD FP751 /MAX –MI	
9	74.03852.00B	IC OSD LSC3852DW SO-W 24P	
10	71.06383.00I	IC LVDS 24B THC63LVDM83A SSOP	IC116 IC117
11	71.09884.009	IC GRAPH DIGIT AD9884 MQFP128P	IC107
12	71.88282.A0E	IC FPD CTRL MX88L282 256P 3.3V	IC111
13	72.02421.001	IC EPROM 24LC21AT-SN SO-N 8P	IC101
14	72.24C04.A01	IC EEPORM 24C04 512K*8 SO-N 8P	IC102
15	72.45161.A09	IC DRAM 4516161AG5-A10-7JFTSOP	IC113 – IC115
16	73.07414.061	IC CMOS 74LVC14A SO-N 14P	IC108
17	74.01117.03M	IC V.R LD1117-3.3 0.8A TO-252	IC106 IC121 IC122
18	82.30006.001	XTAL 12MHZ 32P 30PPM	Y101
19	82.31431.001	XTAL 14.318MHZ 32P 30PPM 49US	Y102
20	84.04431.037	FET MOS SI4431DY-T1 PC SP-8	IC118
21	71.00112.M0C	IC MICON MTV112M PLCC44P/FP558	IC105
22	55.L2204.011	CTROL BD FP751/MAX-MI	
23	39.L0701.012	CVR REAR PC+ABS 002 FP750 T99	
24	50.L1603.001	SIGNAL/C 15/16P 2080MM 002	
25	54.05023.211	INVERTER BD FP730 AMBIT	
26	56.91L16.001	LCDM 17” L170E1/ADT	
27	60.L0704.012	ASSY BASE P042 751/MAXDA T99	
28	60.L0706.001	ASSY SHD CVR FP750/ACER	
29	60.L1601.001	ASSY BKT-R FP730	
30	60.L2201.001	ASSY BZL P042 751/MAXDATA T99	
31	65.L0701.001	ASSY SPEAKER+WIRE 370MM 8Ω 2W	
32	65.L0702.001	ASSY SPEAKER+WIRE 200MM 8Ω 2W	
33	42.L0811.001	PE-HD BAG 600*600*0.05T 24LNG	

Spare Parts List

P/N: 99.L2272.XWG

ITEM	PART NO.	DESCRIPTION	LOCATION
34	27.82718.011	CORD H05VV-F#18*3C 10A250V EUR	
35	44.70A01.091	CTN AB 570*338*621 751/MAXDATA	
36	44.70A02.001	CTN-CVR AB 1148*684 FP850	
37	47.L0701.001	CSN L EPS NATURE FP750	
38	47.L0702.001	CSN R EPS NATURE FP750	
39	49.L2201.022	MANUAL BELINEA 101710 T99 17L	
40	50.74405.501	CABLE AUD PC99PT284C/PT577C BK	



NOTES:
1. Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
2. All resistors are 1/8 watt, 5% except where otherwise indicated
3. ⏚ ⏚ ⏚ Represents PCB common ground.

SIZE A4	L0703A00.SCH	FAB A	Doc.No. 304-C01	REV. 0
DATE : 7/5/2000		Sheet 1 OF 8		
Project Code. 99.L2272.001				
Prepared By	Reviewed By	Approved By		
CHRIS PENG 7/5/00	JAMMY LIN 7/5/00	T.S.WU 7/5/00		

IC116 & IC117	R217	R182
SN75LVDS83	0	0
DS90C383	OPEN	OPEN
THC63LVDM83A	OPEN	OPEN

RA0 RA1 RA2 RA3 — RP118 BEAD — RA00 RA01 RA02 RA03
RA4 RA5 RA6 RA7 — RP119 BEAD — RA04 RA05 RA06 RA07
GA0 GA1 GA2 GA3 — RP120 BEAD — GA00 GA01 GA02 GA03
GA4 GA5 GA6 GA7 — RP121 BEAD — GA04 GA05 GA06 GA07
BA0 BA1 BA2 BA3 — RP122 BEAD — BA00 BA01 BA02 BA03
BA4 BA5 BA6 BA7 — RP123 BEAD — BA04 BA05 BA06 BA07
RB0 RB1 RB2 RB3 — RP124 BEAD — RBE0 RBE1 RBE2 RBE3
RB4 RB5 RB6 RB7 — RP125 BEAD — RBE4 RBE5 RBE6 RBE7
GB0 GB1 GB2 GB3 — RP126 BEAD — GBE0 GBE1 GBE2 GBE3
GB4 GB5 GB6 GB7 — RP127 BEAD — GBE4 GBE5 GBE6 GBE7
BB0 BB1 BB2 BB3 — RP128 BEAD — BBE0 BBE1 BBE2 BBE3
BB4 BB5 BB6 BB7 — RP129 BEAD — BBE4 BBE5 BBE6 BBE7
LCKAA LDTG LVSYNC LHSYNC — RP130 56 — LCK_0 DE LVSYNCB LHSYNCB

IC116 THC63LVDM83A

1	VCC	TXIN4	56
2	TXIN5	TXIN3	55
3	TXIN6	TXIN2	54
4	TXIN7	GND	53
5	GND	TXIN1	52
6	TXIN8	TXIN0	51
7	TXIN9	TXIN27	50
8	TXIN10	LVDS GND	49
9	VCC	TXOUT0-	48
10	TXIN11	TXOUT0+	47
11	TXIN12	TXOUT1-	46
12	TXIN13	TXOUT1+	45
13	GND	LVDS VCC	44
14	TXIN14	LVDS GND	43
15	TXIN15	TXOUT2-	42
16	TXIN16	TXOUT2+	41
17	VCC	TXCLK OUT-	40
18	TXIN17	TXCLK OUT+	39
19	TXIN18	TXOUT3-	38
20	TXIN19	TXOUT3+	37
21	GND	LVDS GND	36
22	TXIN20	PLL GND	35
23	TXIN21	PLL VCC	34
24	TXIN22	PLL GND	33
25	TXIN23	PWR DWM	32
26	VCC	TXCLK IN	31
27	TXIN24	TXIN26	30
28	TXIN25	GND	29

RA07 RA05 GA00 → TXIN7 TXIN5 TXIN6 — RA04 RA03 RA02 ← TXIN4 TXIN3 TXIN2
GA01 GA02 GA06 → TXIN8 TXIN9 TXIN10 — RA01 RA00 RA06 ← TXIN1 TXIN0 TXIN27
GA07 GA03 GA04 → TXIN11 TXIN12 TXIN13 — TXOOUT0- TXOOUT0+ TXOOUT1- TXOOUT1+
GA05 BA00 BA06 → TXIN14 TXIN15 TXIN16 — TXOOUT2- TXOOUT2+ TXOCLK- TXOCLK+
BA07 BA01 BA02 → TXIN17 TXIN18 TXIN19 — TXOOUT3- TXOOUT3+
BA03 BA04 BA05 → TXIN20 TXIN21 TXIN22 — LCK_O DE
LHSYNCB LVSYNCB → TXIN23 TXIN24 — LHSYNCB LVSYNCB

IC117 THC63LVDM83A

1	VCC	TXIN4	56
2	TXIN5	TXIN3	55
3	TXIN6	TXIN2	54
4	TXIN7	GND	53
5	GND	TXIN1	52
6	TXIN8	TXIN0	51
7	TXIN9	TXIN27	50
8	TXIN10	LVDS GND	49
9	VCC	TXOUT0-	48
10	TXIN11	TXOUT0+	47
11	TXIN12	TXOUT1-	46
12	TXIN13	TXOUT1+	45
13	GND	LVDS VCC	44
14	TXIN14	LVDS GND	43
15	TXIN15	TXOUT2-	42
16	TXIN16	TXOUT2+	41
17	VCC	TXCLK OUT-	40
18	TXIN17	TXCLK OUT+	39
19	TXIN18	TXOUT3-	38
20	TXIN19	TXOUT3+	37
21	GND	LVDS GND	36
22	TXIN20	PLL GND	35
23	TXIN21	PLL VCC	34
24	TXIN22	PLL GND	33
25	TXIN23	PWR DWM	32
26	VCC	TXCLK IN	31
27	TXIN24	TXIN26	30
28	TXIN25	GND	29

RBE7 RBE5 GBE0 → TXIN7 TXIN5 TXIN6 — RBE4 RBE3 RBE2 ← TXIN4 TXIN3 TXIN2
GBE1 GBE2 GBE6 → TXIN8 TXIN9 TXIN10 — RBE1 RBE0 RBE6 ← TXIN1 TXIN0 TXIN27
GBE7 GBE3 GBE4 → TXIN11 TXIN12 TXIN13 — TXEOUT0- TXEOUT0+ TXEOUT1- TXEOUT1+
GBE5 BBE0 BBE6 → TXIN14 TXIN15 TXIN16 — TXEOUT2- TXEOUT2+ TXECLK- TXECLK+
BBE7 BBE1 BBE2 → TXIN17 TXIN18 TXIN19 — TXEOUT3- TXEOUT3+
BBE3 BBE4 BBE5 → TXIN20 TXIN21 TXIN22 — LCK_O DE
LHSYNCB LVSYNCB → TXIN23 TXIN24 — LHSYNCB LVSYNCB

EN3U — C220 0.1u 50V (D) — LVDS1_GND
EN3U — C221 0.1U 50V (D) — LVDS1_GND
EN3U — C222 0.1U 50V (D) — LVDS1_GND
EN3U — C225 0.1U 50V (D) — LVDS2_GND
EN3U — C226 0.1U 50V (D) — LVDS2_GND
EN3U — C227 0.1U 50V (D) — LVDS2_GND
L101 BEAD: SGND — LVDS1_GND
L102 BEAD: SGND — LVDS2_GND
R217 OPEN
R182 OPEN

+5V +12V
R223 OPEN
C234 OPEN 50V (D)
R186 100K
IC118 SI4431DY (S S S G D D D D)
F101 2A → 12V-LCD1
+C235 47U 35V (EL)
C236 0.1U 50V (D)
SGND
+5V
IC119 OPEN (S S S G D D D D)
STPS1L30A
D108
LCD_ON
R185 20K
Q104 2N3904
C171 68U 25V (EL)
R155 10K
SGND

LUCC2 — L113 BEAD (0603) — EN3U
C228 0.1U 50V (D) — LVDS1_GND
+C219 22U 16V (EL)
PVCC2 — L115 BEAD (0603)
C230 0.1U 50V (D) — LVDS1_GND
+C229 22U 16V (EL)
LUCC1 — L117 BEAD (0603) — EN3U
C231 0.1U 50V (D) — LVDS2_GND
+C232 22U 16V (EL)
PVCC1 — L119 BEAD (0603)
+C224 22U 16V (EL)
C233 0.1U 50V (D) — LVDS2_GND

+5V — BEAD L112 — IC121 LD33 (VI VO GND) — EN3U
C223 0.1U 50V (D)
+C128 22U 16V (EL)
C247 0.1U 50V (D)

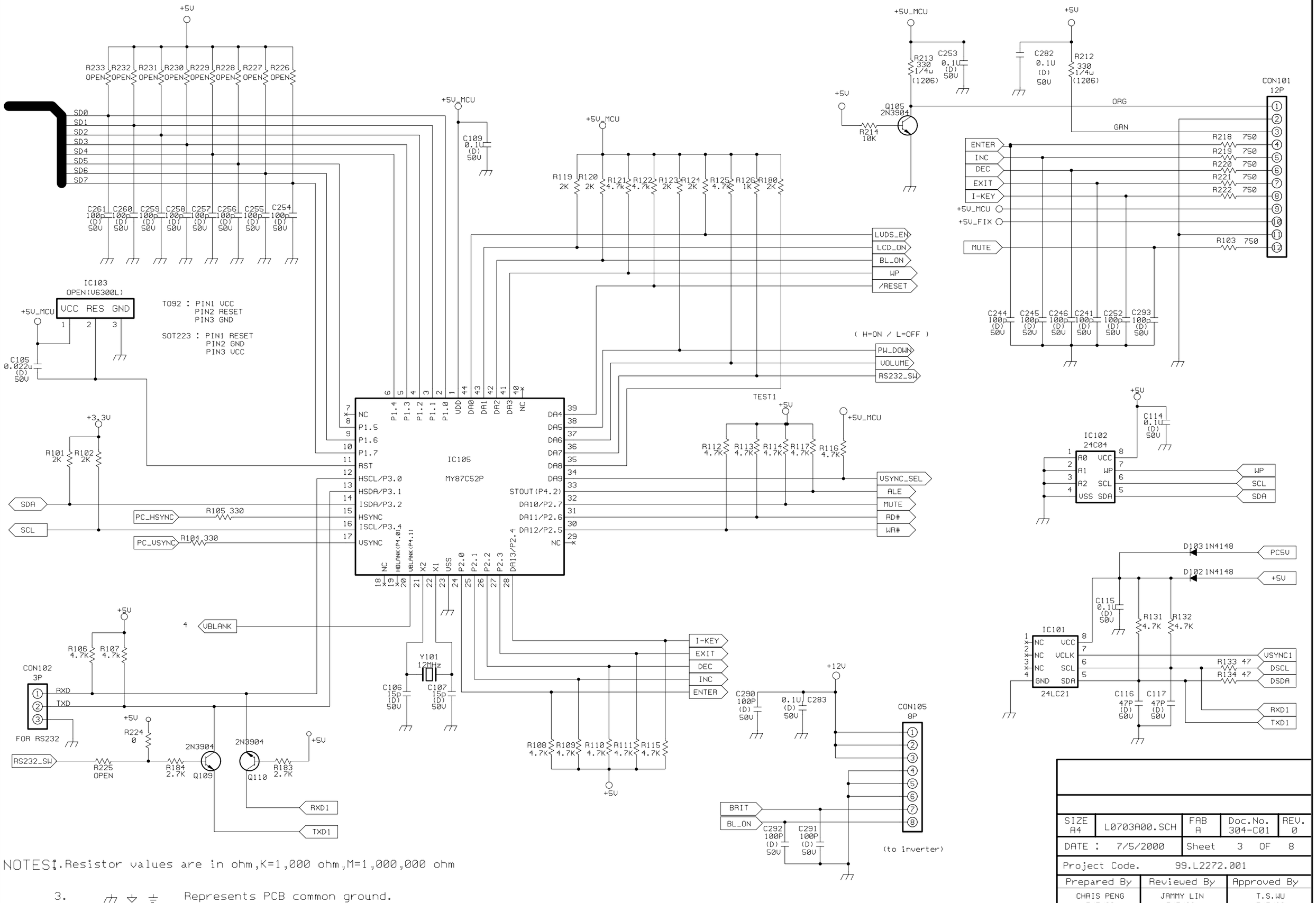
CON103 20P

TXC-		1	TX2+
TXC+		2	TX2-
TXOOUT0-	TXOOUT0-	3	TXEOUT3+
TXOOUT0+	TXOOUT0+	4	TXEOUT3-
TXOOUT1-	TXOOUT1-	5	TXECLK+
TXOOUT1+	TXOOUT1+	6	TXECLK-
TXOOUT2-	TXOOUT2-	7	TXEOUT2+
TXOOUT2+	TXOOUT2+	8	TXEOUT2-
TXOCLK-	TXOCLK-	9	TXEOUT1+
TXOCLK+	TXOCLK+	10	TXEOUT1-
TXOOUT3-	TXOOUT3-	11	TXEOUT0+
TXOOUT3+	TXOOUT3+	12	TXEOUT0-
TX0-		13	TX1+
TX0+		14	TX1-

LVDS_EN
(LVDS & TMDS output)

+3.3V
R187 4.7K
R188 4.7K
SGND
CON104 6P (1 2 3 4 5 6)
12V-LCD1
(To LCD panel)
SGND

SIZE A4	L0703A00.SCH	FAB A	Doc.No. 304-C01	REV. 0
DATE : 7/5/2000		Sheet	2 OF 8	
Project Code.	99.L2272.001			
Prepared By	Reviewed By	Approved By		
CHRIS PENG 7/5/00	JAMMY LIN 7/5/00	T.S.WU 7/5/00		



TO92 : PIN1 VCC
PIN2 RESET
PIN3 GND

SOT223 : PIN1 RESET
PIN2 GND
PIN3 VCC

NOTES: 1. Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm

3. Represents PCB common ground.

SIZE A4	L0703A00.SCH	FAB A	Doc.No. 304-C01	REV. 0
DATE : 7/5/2000		Sheet 3 OF 8		
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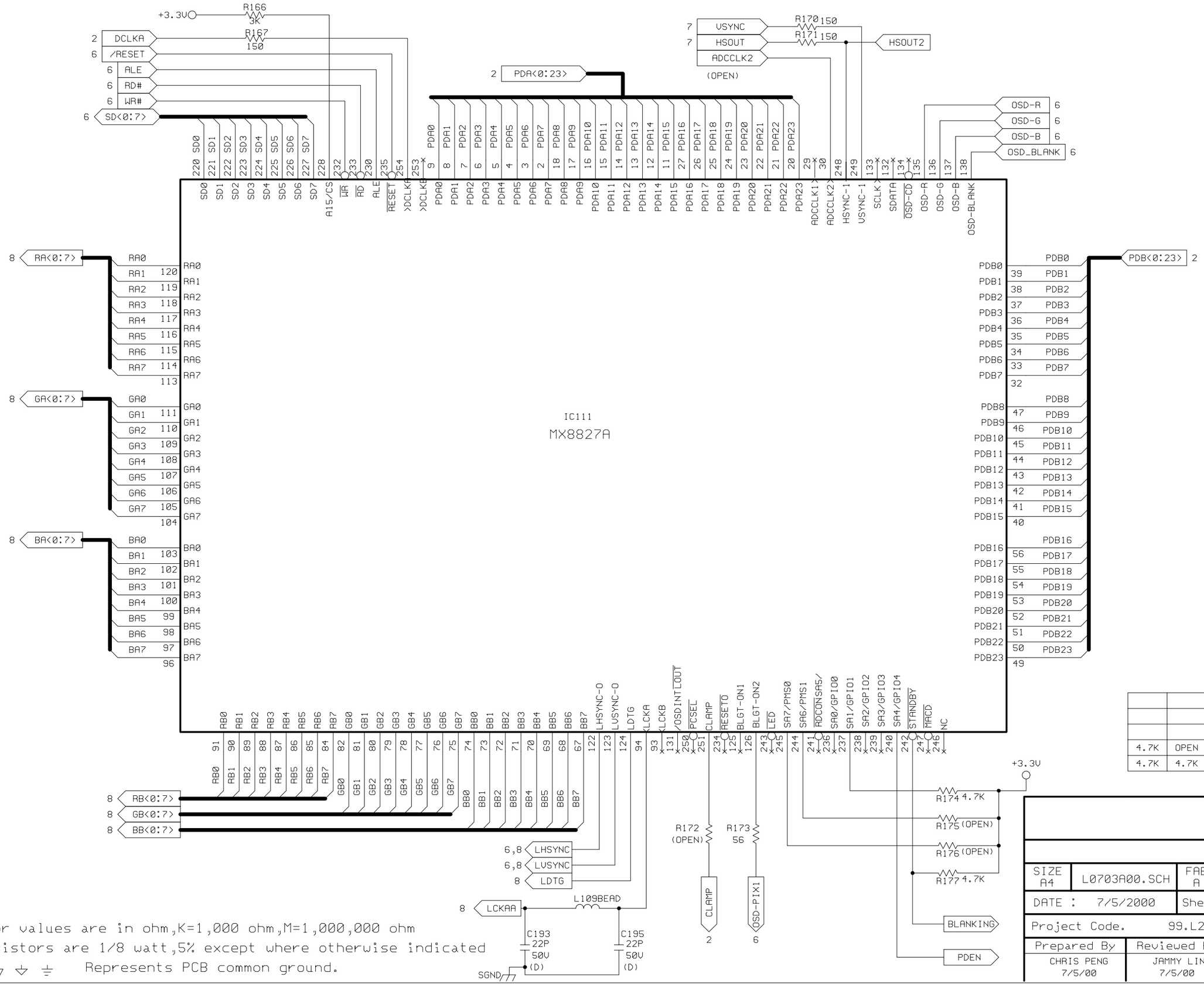
CHRIS PENG 7/5/00	JAMMY LIN 7/5/00	T.S.WU 7/5/00		

IC107
AD9884

PCSV
DSDA
DSCL
VSYNC1
PC_VSYNC
PC_HSYNC
ADCCLK2
COAST
PDEN
HSOUT2
CLAMP
SDA
SCL
HSOUT
DCLKA
PDA<0:23>
PDB<8:23>
PDB<16:23>

NOTES: 1. Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
2. All resistors are 1/8 watt, 5% except where otherwise indicated
3. ⏚ ⏚ ⏚ Represents PCB common ground.

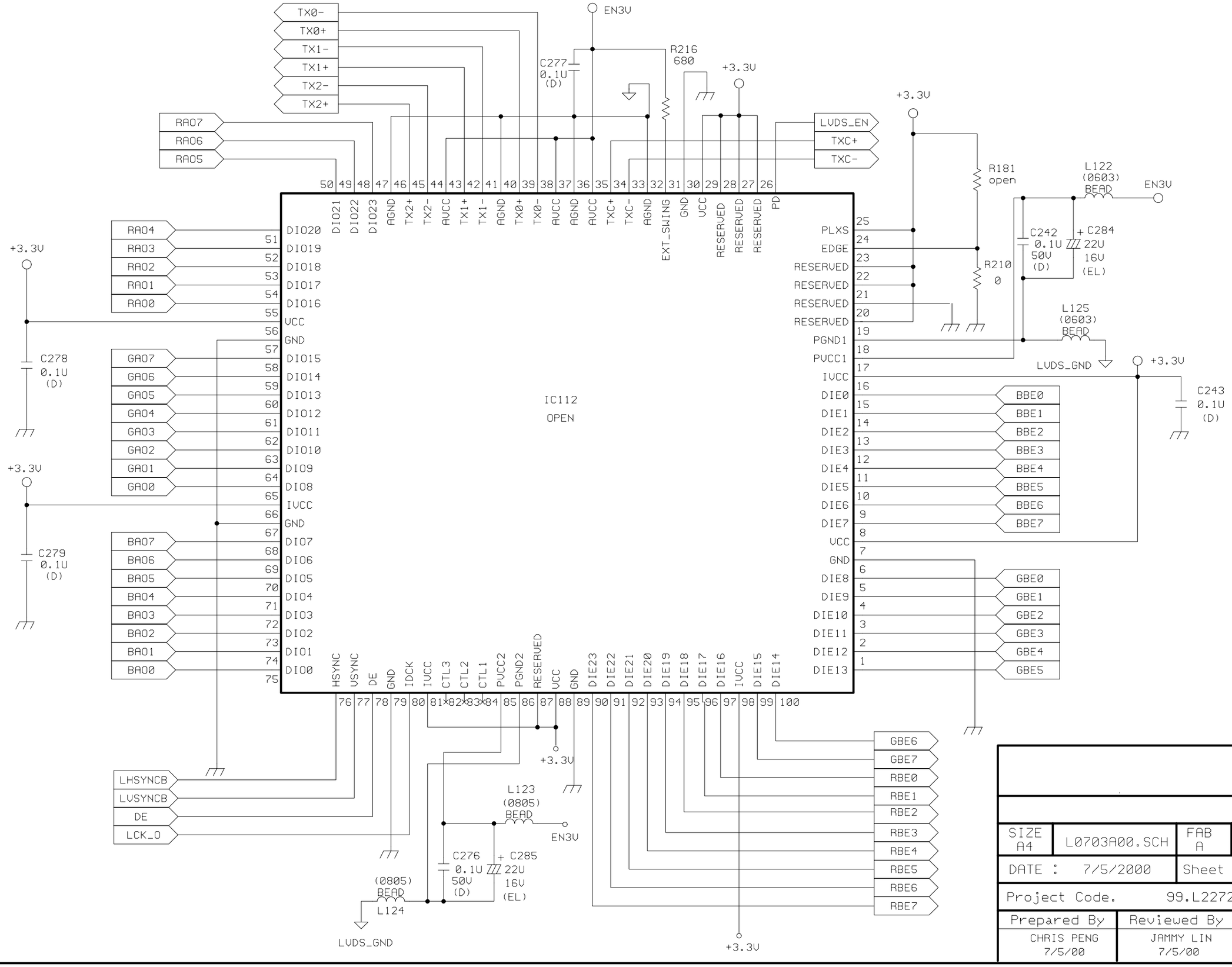
SIZE A4	L0703A00.SCH	FAB A	Doc.No. 304-C01	REV. 0
DATE : 7/5/2000		Sheet 4 OF 8		
Project Code. 99.L2272.001				
Prepared By	Reviewed By	Approved By		
CHRIS PENG 7/5/00	JAMMY LIN 7/5/00	T.S.WU 7/5/00		



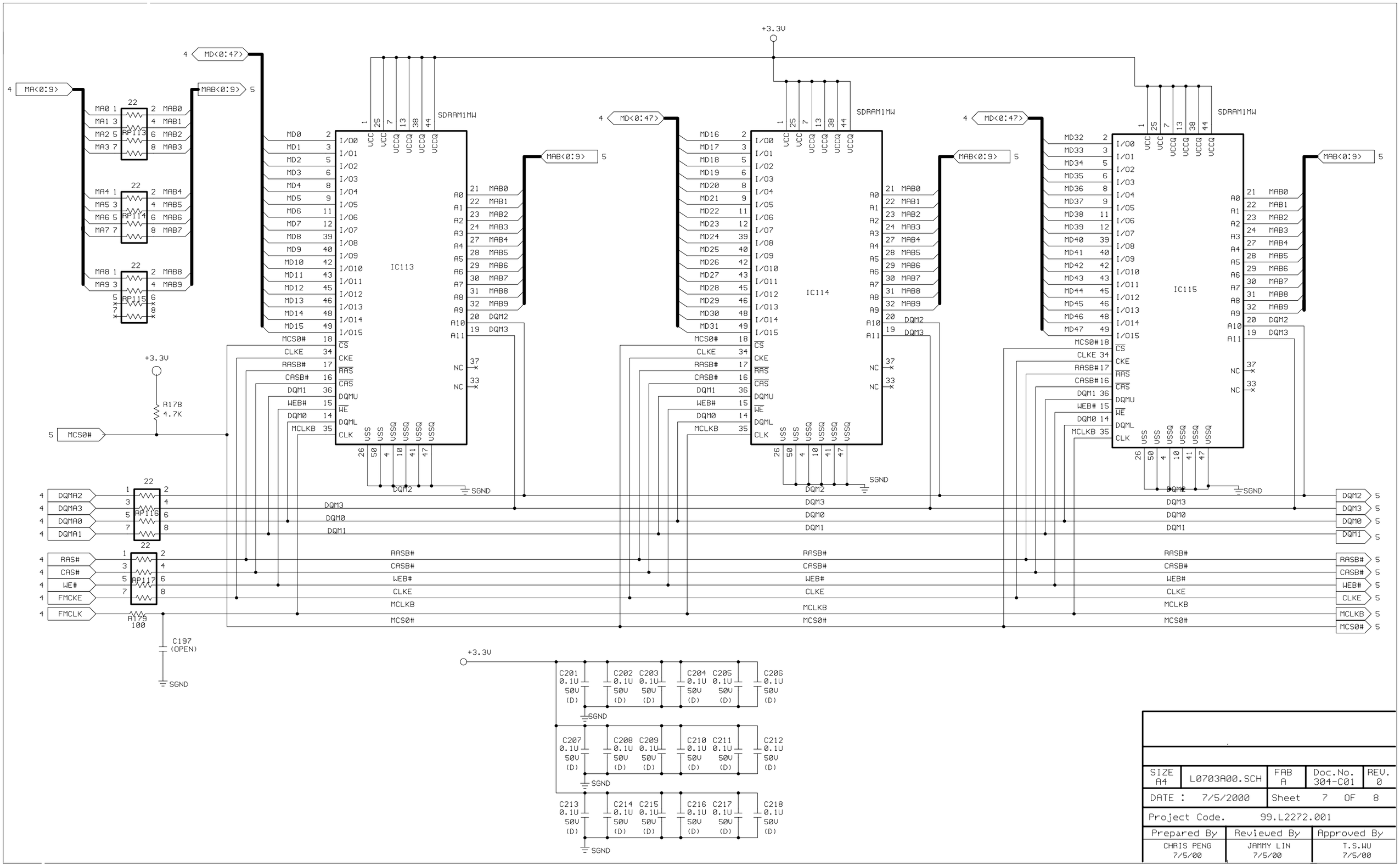
		FUNCTION
		Direct mode
		Reserved
4.7K	OPEN	Indirect mode
4.7K	4.7K	Serial Bus

NOTES:
1. Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
2. All resistors are 1/8 watt, 5% except where otherwise indicated
3. ⏚ ⏚ ⏚ Represents PCB common ground.

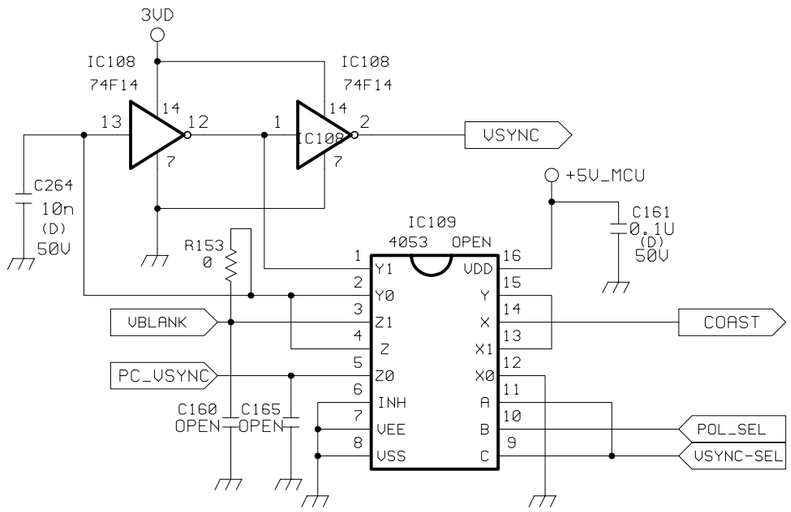
SIZE A4	L0703A00.SCH	FAB A	Doc.No. 304-C01	REV. 0
DATE : 7/5/2000		Sheet 5 OF 8		
Project Code. 99.L2272.001				
Prepared By	Reviewed By	Approved By		
CHRIS PENG 7/5/00	JAMMY LIN 7/5/00	T.S.WU 7/5/00		

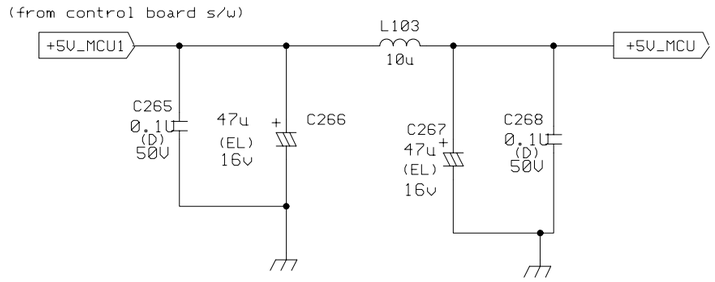


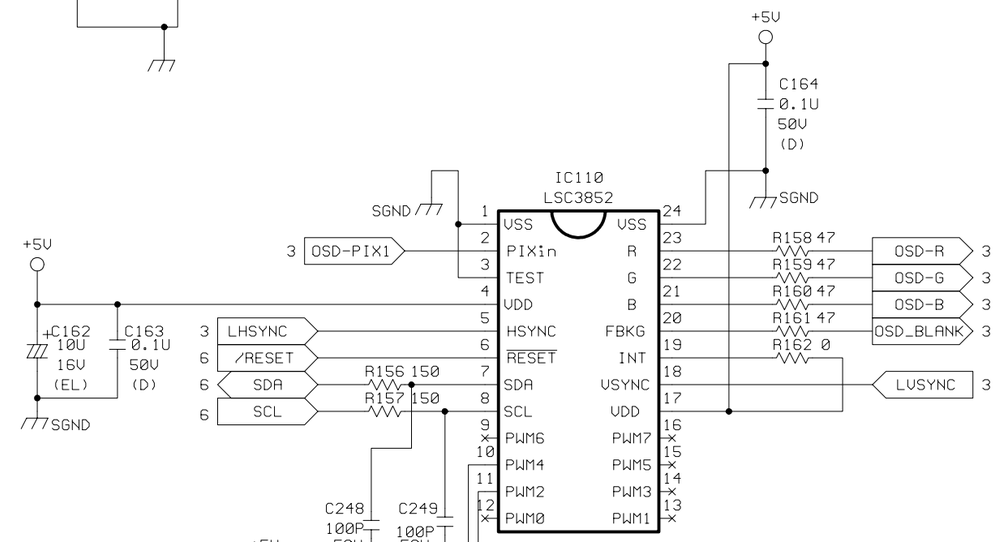
SIZE A4	L0703A00.SCH	FAB A	Doc.No. 304-C01	REV. 0
DATE : 7/5/2000		Sheet 6 OF 8		
Project Code. 99.L2272.001				
Prepared By	Reviewed By	Approved By		
CHRIS PENG 7/5/00	JAMMY LIN 7/5/00	T.S.WU 7/5/00		

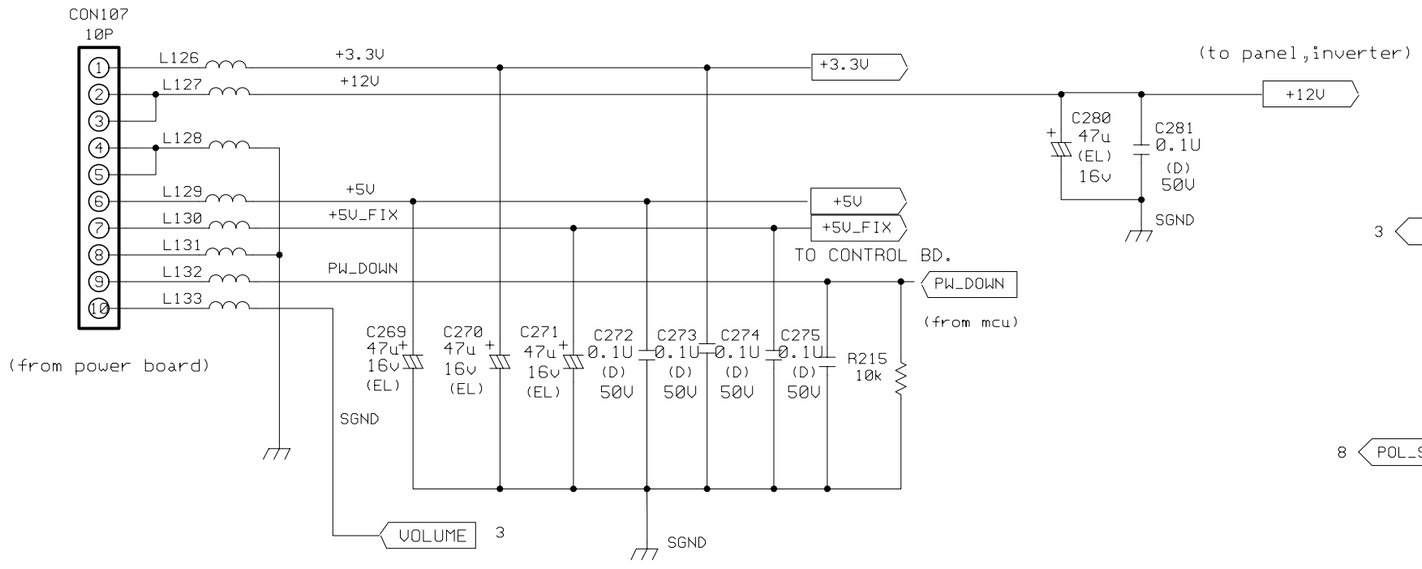


SIZE A4	L0703A00.SCH	FAB A	Doc.No. 304-C01	REV. 0
DATE : 7/5/2000		Sheet 7 OF 8		
Project Code. 99.L2272.001				
Prepared By	Reviewed By	Approved By		
CHRIS PENG 7/5/00	JAMMY LIN 7/5/00	T.S.WU 7/5/00		









NOTES: 1. Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
2. All resistors are 1/8 watt, 5% except where otherwise indicated
3. ⏚ ⏚ ⏚ Represents PCB_common ground.

SIZE A4	L0703A00.SCH	FAB A	Doc.No. 304-C01	REV. 0
DATE : 7/5/2000		Sheet 8 OF 8		
Project Code. 99.L2272.001				
Prepared By	Reviewed By	Approved By		
CHRIS PENG 7/5/00	JAMMY LIN 7/5/00	T.S.WU 7/5/00		